

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("6657665").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/05 17:21
L2	21	("5012344"   "5471515"   "5587596"   "5608243"   "5625210"   "5631704"   "5698844"   "5721425"   "5760458"   "5786623"   "5789774"   "5838650"   "5847422"   "5872371"   "6150704"   "6184516"   "6466266").PN. OR ("6657665"). URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/05 17:47
L3	15	circuit with transfer with source follower with reset and @ay<="1999"	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/05 17:49
L4	10	circuit with transfer with source follower with reset and @ay<="1999" and "348".clas.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/05 18:05
L5	45	transfer with source follower with reset and @ay<="1999" and "348".clas.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/05 18:11
S1	147	retrograde well and cmos and @ay<="1999"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:02
S2	140	retrograde well and cmos and @ay<="1999" not durcan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:03
S3	9	retrograde well and cmos and @ay<="1999" and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:25

## EAST Search History

S4	38	retrograde and cmos and @ay<="1999" and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:26
S5	45	retrograde and @ay<="1999" and sensor and pixel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:26
S6	85	retrograde and @ay<="1999" and pixel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:31
S7	173	(doping with higher) and @ay<="1999" and pixel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:32
S8	40	S6 not S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:31
S9	39	(doping with higher) and @ay<="1999" and (reset with transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:42
S10	919	retrograde and photo\$7 and @ay<="1999"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:42
S11	274	retrograde and photo\$8 and @ay<="1999" and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:45

## EAST Search History

S12	96	(retrograde with dop\$4) and photo\$8 and @ay<="1999" and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:47
S13	360	(257/231).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 18:56
S14	0	S13 and retrograde and @ay<="1999"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:56
S15	321	S13 and @ay<="1999"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:57
S16	14	S13 and @ay<="1999" and (dop\$4 with higher)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 19:00
S17	19	S13 and @ay<="1999" and (gradient)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 18:59
S18	632	(257/233).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 19:00
S19	1014	(257/291).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 19:00

## EAST Search History

S20	1187	(257/292).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 19:00
S21	0	("18and@ay<=1999and(dop\$4with higher)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 19:00
S22	25	S18 and @ay<="1999" and (dop\$4 with higher)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 19:03
S23	2	(S19 or S20) and @ay<="1999" and retrograde	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 19:01
S24	1	S18 and @ay<="1999" and retrograde	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 19:01
S25	25	("5461425"   "5471515"   "5548773"   "5665959"   "5780884"   "5841125"   "5841126"   "5850195"   "5859450"   "5861645"   "5861655"   "5869857"   "5880691"   "5886659"   "5949483"   "5965871"   "5969758"   "5981932"   "5990506"   "5995163"   "6072206"   "6090639").PN. OR ("6465862").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/03 19:02
S26	1	S25 and retrograde	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/03 19:01
S27	9	S25 and concentration	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/10/03 19:02

## EAST Search History

S28	44	"6787819" "6858460" "6445014" "7166878" "6930338" US-4374700-\$.DID. OR US-5150177-\$.DID. OR US-5151385-\$.DID. OR US-5268316-\$.DID. OR US-5293237-\$.DID. OR US-5319604-\$.DID. OR US-5428239-\$.DID. OR US-5461425-\$.DID. OR US-5541402-\$.DID. OR US-5576763-\$.DID. OR US-5608243-\$.DID. OR US-5614744-\$.DID. OR US-5625210-\$.DID. OR US-5705846-\$.DID. OR US-5708263-\$.DID. OR US-5747840-\$.DID. OR US-5757045-\$.DID. OR US-5773863-\$.DID. OR US-4963973-\$.DID. OR US-5814866-\$.DID. OR US-5859450-\$.DID. OR US-6147366-\$.DID. OR US-6140683-\$.DID. OR US-6268250-\$.DID. "6109734" "5030317" "4775445" "5478606" "5758417"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 20:12
S29	20	((("6787819") or ("6858460") or ("6445014") or ("7166878") or ("6930338") or ("6109734") or ("5030317") or ("4775445") or ("5478606") or ("5758417"))).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 20:12
S30	18	(S28 or S29) and retrograde	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 20:12
S31	2	("5656514").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 20:34
S32	2	("6093951").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 20:37

## EAST Search History

S33	2	("5773863").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/03 20:37
S34	2	10/295952	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 21:08
S37	6	((("6787819") or ("6483129") or ("6310366")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/05 17:21

Day : Friday  
Date: 10/5/2007

Time: 18:51:02

# **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = RHODES

First Name = HOWARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08066835</a>	5405788	150	05/24/1993	METHOD FOR FORMING AND TAILORING THE ELECTRICAL CHARACTERISTICS OF SEMICONDUCTOR DEVICES	RHODES, HOWARD
<a href="#">08416994</a>	Not Issued	166	04/05/1995	METHOD FOR FORMING AND TAILORING THE ELECTRICAL CHARACTERISTICS OF SEMICONDUCTOR DEVICES	RHODES, HOWARD
<a href="#">08578825</a>	Not Issued	161	12/26/1995	METHOD TO AVOID THRESHOLD VOLTAGE SHIFT IN THICKER DIELECTRIC FILMS	RHODES, HOWARD
<a href="#">08627262</a>	5750012	150	04/04/1996	MULTIPLE SPECIES SPUTTERING FOR IMPROVED BOTTOM COVERAGE AND IMPROVED SPUTTER RATE	RHODES, HOWARD
<a href="#">08763848</a>	5661045	150	12/09/1996	METHOD FOR FORMING AND TAILORING THE ELECTRICAL CHARACTERISTICS OF SEMICONDUCTOR DEVICES	RHODES, HOWARD
<a href="#">09053354</a>	6083358	150	04/01/1998	MULTIPLE SPECIES SPUTTERING FOR IMPROVED BOTTOM COVERAGE AND IMPROVED SPUTTER RATE	RHODES, HOWARD
<a href="#">09312373</a>	6462394	150	05/13/1999	DEVICE CONFIGURED TO AVOID THRESHOLD VOLTAGE SHIFT IN A DIELECTRIC FILM	RHODES, HOWARD
<a href="#">09609441</a>	6398923	150	07/03/2000	MULTIPLE SPECIES SPUTTERING METHOD	RHODES, HOWARD
<a href="#">09654093</a>	7067442	150	08/31/2000	METHOD TO AVOID THRESHOLD VOLTAGE SHIFT IN THICKER DIELECTRIC FILMS	RHODES, HOWARD

<u>09721839</u>	<u>6458714</u>	150	11/22/2000	METHOD OF SELECTIVE OXIDATION IN SEMICONDUCTOR MANUFACTURE	RHODES, HOWARD
<u>09789335</u>	<u>6518610</u>	150	02/20/2001	RHODIUM-RICH OXYGEN BARRIERS	RHODES, HOWARD
<u>09945252</u>	<u>6630706</u>	150	08/30/2001	LOCALIZED ARRAY THRESHOLD VOLTAGE IMPLANT TO ENHANCE CHARGE STORAGE WITHIN DRAM MEMORY CELLS	RHODES, HOWARD
<u>10209386</u>	<u>6740554</u>	150	07/30/2002	METHODS TO FORM RHODIUM-RICH OXYGEN BARRIERS	RHODES, HOWARD
<u>10234576</u>	<u>6800520</u>	150	08/30/2002	LOCALIZED ARRAY THRESHOLD VOLTAGE IMPLANT ENHANCE CHARGE STORAGE WITHIN DRAM MEMORY CELLS	RHODES, HOWARD
<u>10291728</u>	<u>6818930</u>	150	11/12/2002	GATED ISOLATION STRUCTURE FOR IMAGERS	RHODES, HOWARD
<u>10291772</u>	<u>6888214</u>	150	11/12/2002	ISOLATION TECHNIQUES FOR REDUCING DARK CURRENT IN CMOS IMAGE SENSORS	RHODES, HOWARD
<u>10293494</u>	<u>7091536</u>	150	11/14/2002	ISOLATION PROCESS AND STRUCTURE FOR CMOS IMAGERS	RHODES, HOWARD
<u>10303897</u>	<u>6960796</u>	150	11/26/2002	CMOS IMAGER PIXEL DESIGNS WITH STORAGE CAPACITOR	RHODES, HOWARD
<u>10318597</u>	<u>6781175</u>	150	12/12/2002	RHODIUM-RICH INTEGRATED CIRCUIT CAPACITOR ELECTRODE	RHODES, HOWARD
<u>10345155</u>	<u>7087944</u>	150	01/16/2003	IMAGE SENSOR HAVING A CHARGE STORAGE REGION PROVIDED WITHIN AN IMPLANT REGION	RHODES, HOWARD
<u>10385844</u>	<u>6949445</u>	150	03/12/2003	METHOD OF FORMING ANGLED IMPLANT FOR TRENCH ISOLATION	RHODES, HOWARD
<u>10422965</u>	Not Issued	71	04/25/2003	Imager floating diffusion region and process for forming same	RHODES, HOWARD
<u>10437494</u>	<u>6815287</u>	150	05/14/2003	LOCALIZED ARRAY THRESHOLD VOLTAGE IMPLANT TO ENHANCE CHARGE STORAGE WITHIN	RHODES, HOWARD



				DRAM MEMORY CELLS	
<a href="#"><u>10632916</u></a>	<a href="#"><u>6878568</u></a>	150	08/04/2003	CMOS IMAGER AND METHOD OF FORMATION	RHODES, HOWARD
<a href="#"><u>10655219</u></a>	<a href="#"><u>6969631</u></a>	150	09/05/2003	METHOD OF FORMING PHOTODIODE WITH SELF-ALIGNED IMPLANTS FOR HIGH QUANTUM EFFICIENCY	RHODES, HOWARD
<a href="#"><u>10660565</u></a>	Not Issued	61	09/12/2003	CMOS imager pixel designs	RHODES, HOWARD
<a href="#"><u>10771290</u></a>	Not Issued	93	02/05/2004	GATED ISOLATION STUCTURE FOR IMAGERS	RHODES, HOWARD
<a href="#"><u>10850664</u></a>	<a href="#"><u>7038263</u></a>	150	05/21/2004	INTEGRATED CIRCUITS WITH RHODIUM-RICH STRUCTURES	RHODES, HOWARD
<a href="#"><u>10925917</u></a>	Not Issued	71	08/26/2004	Isolation techniques for reducing dark current in CMOS image sensors	RHODES, HOWARD
<a href="#"><u>10926358</u></a>	<a href="#"><u>7239003</u></a>	150	08/26/2004	ISOLATION TECHNIQUES FOR REDUCING DARK CURRENT IN CMOS IMAGE SENSORS	RHODES, HOWARD
<a href="#"><u>10983971</u></a>	Not Issued	160	11/08/2004	Localized array threshold voltage implant to enhance charge storage within DRAM memory cells	RHODES, HOWARD
<a href="#"><u>11009006</u></a>	<a href="#"><u>7217589</u></a>	150	12/13/2004	DEEP PHOTODIODE ISOLATION PROCESS	RHODES, HOWARD
<a href="#"><u>11043998</u></a>	Not Issued	93	01/28/2005	IMAGER FLOATING DIFFUSION REGION AND PROCESS FOR FORMING SAME	RHODES, HOWARD
<a href="#"><u>11066325</u></a>	Not Issued	160	02/25/2005	Isolation techniques for reducing dark current in CMOS image sensors	RHODES, HOWARD
<a href="#"><u>11165490</u></a>	<a href="#"><u>7102180</u></a>	150	06/24/2005	CMOS IMAGER PIXEL DESIGNS	RHODES, HOWARD
<a href="#"><u>11206125</u></a>	<a href="#"><u>7195947</u></a>	150	08/18/2005	PHOTODIODE WITH SELF-ALIGNED IMPLANTS FOR HIGH QUANTUM EFFICIENCY AND METHOD OF FORMATION	RHODES, HOWARD
<a href="#"><u>11242375</u></a>	Not Issued	30	10/03/2005	Method to avoid threshold voltage shift in thicker dielectric films	RHODES, HOWARD
<a href="#"><u>11434767</u></a>	Not Issued	30	05/17/2006	Image sensor having a charge storage region provided within an implant region	RHODES, HOWARD

<a href="#">11488845</a>	Not Issued	41	07/19/2006	CMOS imager pixel designs	RHODES, HOWARD
<a href="#">11529631</a>	Not Issued	30	09/29/2006	Method of operating image sensor	RHODES, HOWARD
<a href="#">11636979</a>	Not Issued	17	12/12/2006	Photodiode with self-aligned implants for high quantum efficiency and method of formation	RHODES, HOWARD
<a href="#">11708067</a>	Not Issued	30	02/20/2007	Photodiode with self-aligned implants for high quantum efficiency and method of formation	RHODES, HOWARD
<a href="#">60478348</a>	Not Issued	159	06/16/2003	Photodiode with self-aligned implants for high quantum efficiency and method of formation	RHODES, HOWARD
<a href="#">60947347</a>	Not Issued	20	06/29/2007	HIGH DYNAMIC RANGE (HDR) SENSOR WITH OVERFLOW DRAIN	RHODES, HOWARD
<a href="#">06130075</a>	<a href="#">4307922</a>	150	03/13/1980	MOVABLE STORAGE SYSTEM	RHODES, HOWARD A.
<a href="#">09207593</a>	<a href="#">6639261</a>	150	12/08/1998	METHOD FOR FORMING A LOW LEAKAGE CONTACT IN A CMOS IMAGER	RHODES, HOWARD E
<a href="#">09310489</a>	<a href="#">6159790</a>	150	05/12/1999	METHOD OF CONTROLLING OUTDIFFUSION IN A DOPED THREE-DIMENSIONAL FILM BY USING ANGLED IMPLANTS	RHODES, HOWARD E
<a href="#">09310515</a>	<a href="#">6211545</a>	150	05/12/1999	A DEVICE FABRICATED BY A METHOD OF CONTROLLING OUTDIFFUSION FROM A DOPED THREE-DIMENSIONAL FILM	RHODES, HOWARD E.
<a href="#">09513470</a>	<a href="#">6407440</a>	150	02/25/2000	PIXEL CELL WITH HIGH STORAGE CAPACITANCE FOR A CMOS IMAGER	RHODES, HOWARD E.
<a href="#">09516819</a>	<a href="#">6285038</a>	150	03/01/2000	Integrated circuitry and dram integrated circuitry	RHODES, HOWARD E.

[Search and Display More Records.](#)

**Search Another: Inventor**

<b>Last Name</b>	<b>First Name</b>	
<input type="text" value="RHODES"/>	<input type="text" value="HOWARD"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Friday  
Date: 10/5/2007


**PALM INTRANET**

Time: 18:51:13

**Inventor Name Search Result**

Your Search was:

Last Name = DURCAN

First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07526079</u>	Not Issued	161	05/10/1990	METHOD FOR REDUCING CROSS-SECTIONAL ASYMMETRY IN SPACER STRIPS USED AS MASK ELEMENTS IN A PROCESS FOR REDUCING MINIMUM PHOTOLITHOGRAPHIC MASKING PITCH	DURCAN, MARK
<u>08862752</u>	<u>6331488</u>	150	05/23/1997	PLANARIZATION PROCESS FOR SEMICONDUCTOR SUBSTRATES	DURCAN, MARK
<u>09146116</u>	<u>6333264</u>	150	09/02/1998	SEMICONDUCTOR PROCESSING METHOD USING HIGH PRESSURE LIQUID MEDIA TREATMENT	DURCAN, MARK
<u>09311914</u>	<u>6100162</u>	150	05/14/1999	METHOD OF FORMING A CIRCUITRY ISOLATION REGION WITHIN A SEMICONDUCTIVE WAFER	DURCAN, MARK
<u>09334261</u>	<u>6310366</u>	150	06/16/1999	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<u>09518558</u>	<u>6340624</u>	150	03/03/2000	Method of forming a circuitry isolation region within a semiconductive wafer	DURCAN, MARK
<u>09603849</u>	<u>6417102</u>	150	06/26/2000	SEMICONDUCTOR PROCESSING METHOD USING HIGH PRESSURE LIQUID MEDIA TREATMENT	DURCAN, MARK
<u>09645582</u>	<u>6445014</u>	150	08/25/2000	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<u>09832560</u>	<u>6743724</u>	150	04/11/2001	PLANARIZATION PROCESS FOR SEMICONDUCTOR	DURCAN, MARK

				SUBSTRATES	
<a href="#">09918450</a>	<a href="#">6483129</a>	150	08/01/2001	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<a href="#">10180088</a>	<a href="#">6686220</a>	150	06/27/2002	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<a href="#">10291670</a>	<a href="#">6858460</a>	150	11/12/2002	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<a href="#">10295952</a>	<a href="#">6787819</a>	150	11/18/2002	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN, MARK
<a href="#">10705707</a>	<a href="#">6806137</a>	150	11/11/2003	TRENCH BURIED BIT LINE MEMORY DEVICES AND METHODS THEREOF	DURCAN, MARK
<a href="#">10761319</a>	Not Issued	30	01/22/2004	Retrograde well structure for a CMOS imager	DURCAN, MARK
<a href="#">10838545</a>	Not Issued	41	05/04/2004	Planarization process for semiconductor substrates	DURCAN, MARK
<a href="#">10968429</a>	<a href="#">7170124</a>	150	10/19/2004	TRENCH BURIED BIT LINE MEMORY DEVICES AND METHODS THEREOF	DURCAN, MARK
<a href="#">11484809</a>	Not Issued	168	07/11/2006	Planarization process for semiconductor substrates	DURCAN, MARK
<a href="#">11588748</a>	Not Issued	41	10/27/2006	Trench buried bit line memory devices and methods thereof	DURCAN, MARK
<a href="#">09503638</a>	<a href="#">6777732</a>	150	02/14/2000	RANDOM ACCESS MEMORY	DURCAN, MARK D.

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** **First Name**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)